

1 What is claimed is:

2 1. An electronic endoscope system comprising:

3 anelectronicendoscope to whose front end an imaging device
4 is set;

5 a processor unit connected with the electronic endoscope
6 to apply a predetermined signal processing to a video signal
7 output from the imaging device;

8 a reference-delay-time generation circuit for generating
9 a signal having a rough reference delay time;

10 a short-delay-time generation circuit for generating a
11 signal having a delay time shorter than a reference delay time
12 of the reference-delay-time generation circuit; and

13 a control circuit for generating a delay signal
14 corresponding to a length of the electronic endoscope in
15 cooperation with these delay-time generation circuits and
16 controlling image processing in accordance with the delay
17 signal.

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19 2. The electronic endoscope system according to claim 1,
20 wherein

21 the short-delay-time generation circuit is provided with
22 a plurality of gate delay devices to set a short delay time
23 according to a delay of a signal passing through the gate delay
24 devices.

1 3. The electronic endoscope system according to claim 1,
2 wherein

3 the control circuit has a first multiplexer for selecting
4 any one of a plurality of drive clock signals generated by the
5 reference-delay-time generation circuit and respectively
6 having a reference delay time and a second multiplexer for
7 selecting any one of a plurality of drive clock signals
8 respectively generated by the short-delay-time generation
9 circuit and respectively having a short delay time, and
10 a delay time corresponding to the length of the electronic
11 endoscope is obtained by controlling the first and second
12 multiplexers.

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14 4. The electronic endoscope system according to claim 1,
15 wherein

16 the control circuit is set to a processor unit, reads
17 delay-time data for a connected electronic endoscope from the
18 electronic endoscope, and generates a necessary delay signal
19 in accordance with the delay-time data.

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